



120

QP 2816
IFWPATENT APPLICATION
Docket No. 9898-204
Client No. SS-16038-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yoon Sim and Jei-Hwan Yoo

Serial No. 09/901,930 Examiner: Terry D. Cunningham

Conf. No. 7100

Filed: July 9, 2001 Art Unit: 2816

For: NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR
MEMORY DEVICE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97 (d) together with:
☐ Statement under 37 CFR 1.97 (e), and
☒ a \$180.00 fee set forth in 37 CFR 1.17 (p).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

09/28/2004 BSAYASI1 00000023 09901930

01 FC:1806

180.00 0P

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Todd J. Iverson
Reg. No. 53,057

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205
503-222-3613

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
Date: September 22, 2004

Angie C. Farr



PATENT APPLICATION
Docket No. 9898-204
Client No. SS-16038-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yoon Sim and Jei-Hwan Yoo

Serial No. 09/901,930 Examiner: Terry D. Cunningham

Conf. No. 7100

Filed: July 9, 2001 Art Unit: 2816

For: NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR
MEMORY DEVICE

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Application</u> <u>Number</u>	<u>Filing date</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
_____	_____	94020909	August 24, 1994	Korea	

OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.</u>
_____	_____	English Language Abstract of Korean Application No: 94020909

Examiner: _____

Date Considered: _____